

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	Process for Manufacturing Printed Circuit Boards with Protected Spaces Between Tracks							
Application Number :								
Date :								
First Named Applicant:	Jose Luis Cubero Pitel							
Confirmation Number:								
Attorney Docket Number:	ES200203037							
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<table border="1"><tr><td>Submitted By:</td><td>Elec. Sign.</td><td>Sign. Capacity</td></tr><tr><td>Bruce E Harang Registered Number: 29720</td><td>Bruce E Harang</td><td>Attorney</td></tr></table>			Submitted By:	Elec. Sign.	Sign. Capacity	Bruce E Harang Registered Number: 29720	Bruce E Harang	Attorney
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Documents being submitted:	Files
us-fee-sheet	ES200203037-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-request	ES200203037-usrequ.xml us-request.dtd us-request.xsl
application-body	ES200203037-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd ES37_Figs_1-3.TIF ES37_Figs_4-6.TIF
Comments	